

DEVELOPMENT OF A PC BASED
8085
HARDWARE TESTER

by

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Project Report
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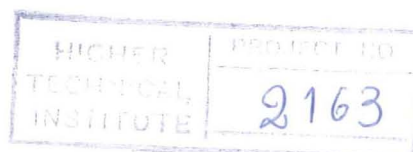
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Summary

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Project Title: Development of a PC based 8085 hardware tester.

The objectives of this project were to design and construct an interface to the IBM-PC that would generate the required signals for emulating the 8085 CPU and finally to write and develop a software interface to the hardware.

The project relies on both hardware and software. The first four chapters are related to the hardware. To be more specific the IBM-PC is emphasized and various signals are explained, an introduction to interfacing and the Emulator interfacing, the required signals for emulating the 8085 CPU and finally the micro target board (Kimperry Cart) is investigated.

In the fifth chapter the various methods of implementing the software for the emulator are explained. Finally the software analysis and the in-circuit emulator requires are mentioned.

In the sixth chapter the various debugging facilities of this project are explained.

At the last chapter the testing and the troubleshooting of the various parts of the emulator are examined.

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