

Microprocessor Monitor
for I/O interface

Project report submitted by
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ABSTRACT

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Project title :Microprocessor Monitor for I/O Interface

This project is concerned with synthesizing an existing 8085 microprocessor unit with interfacing circuits. Also it deals with writing the necessary software to set up communication between them.

This project assumes that the reader have some background in digital logic and microprocessor and can use project number E-553 as Reference.

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