



## Abstract

The aim of this project is to construct and test a digital capacitance meter.

In chapter 1 digital circuits related to this project were examined. In chapter 2 the block diagram and the basic ideas behind this project were explained. In chapter 3 each part of the actual circuit was analysed and finally the complete circuit diagram was explained also the PCB design was discussed. In chapter 4 the procedure followed to check test and calibrate the circuit was illustrated. In Appendix A the data sheets of the I.C. used are available.

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