

HIGHER TECHNICAL INSTITUTE

ELECTRICAL ENGINEERING COURSE

DIPLOMA PROJECT

DEVELOPMENT OF A MICROPROCESSOR
CENTRAL SECURITY SYSTEM

E/734

Design By

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Leontiou Nicos

SUMMARY

PROJECT NUMBER: E/734

TITLE: DEVELOPMENT OF A MICROPROCESSOR CENTRAL SECURITY SYSTEM

DESIGNER: Mr NICOS LEONTIOU

The MICROPROCESSOR CONTROL SECURITY SYSTEM designed, finds many applications in real life, and can overcome with a lot of cases of violation according to the detectors used. The system was design according to the following project requirements:

- i. The hardware and software must be based on an 8085 microprocessor
- ii. The system must be limited to a number of cases depending on the design proposed by the student.

The MICROPROCESSOR CONTROL SECURITY SYSTEM reads the incoming signals from the detectors and decides whether there is a violation or not. If YES it escapes from scanning loop and sends warning message, sets the alarm in stand by mode and waits for the password. If within a certain time the password is not inserted through the 20-key keyboard the alarm will be activated. If the password is entered then the system is disabled until the user enables it again. The enabling of the system is in such a way designed, so as to allow the user a time interval for to exit the building, if he wishes, before is enabled. As other options, two function keys are provided used to read from and write in memory, basically used to change the password code.

I personally believe that this can be a promising MICROPROCESSOR CENTRAL SECURITY SYSTEM and I say this because the design of the system makes it extremely flexible, since it is completely software controlled, and hence ready for all improvements.

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