

IC TESTER USING AN IBM PC

Project report and construction submitted by:

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SUMMARY

IC TESTER USING AN IBM PC

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The purpose of this project is to design, construct and test an IC tester using an IBM PC and to write the appropriate software to drive the relevant hardware. The tester should be capable of carrying out logic function tests on a number of integrated circuits housed in DIL (dual in line) packages which have similar pin configuration.

Designing an IC tester using an IBM PC is not easy since a safe link between the IC under test and the PC is required. Also the tester should be able to handle all possible faults of the IC under test and probable user mistakes, otherwise there is the risk of causing the internal 5V supply voltage of the computer to break down with all the costly consequences for the PC or causing damage to the tester circuit and to the IC under test.

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